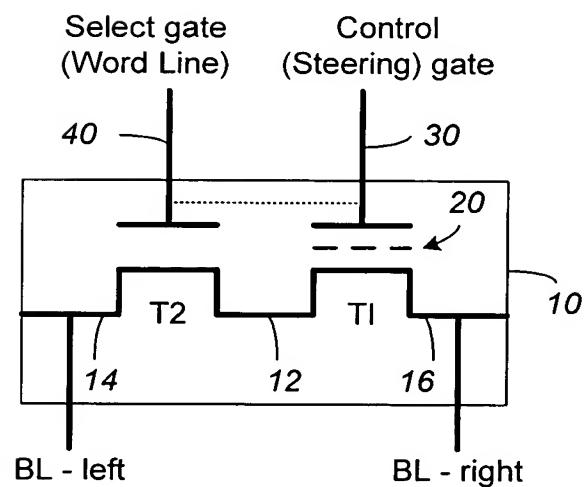
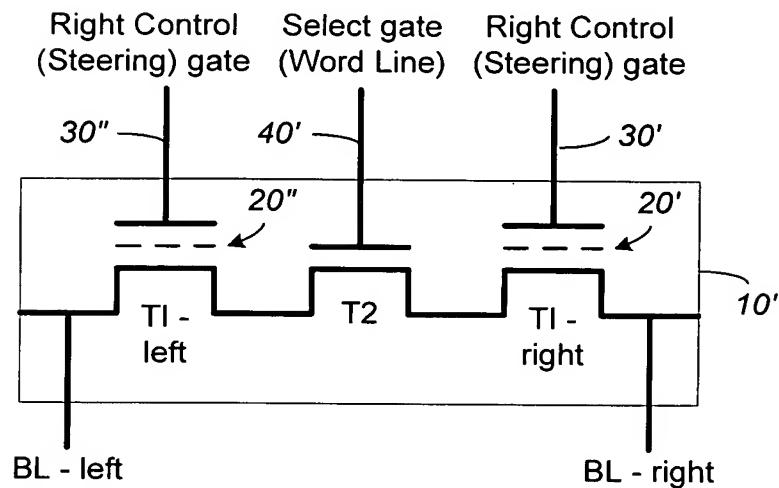


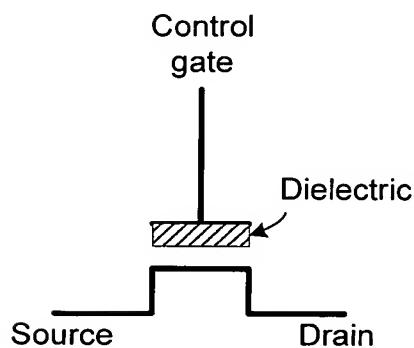
**FIG. 1A**



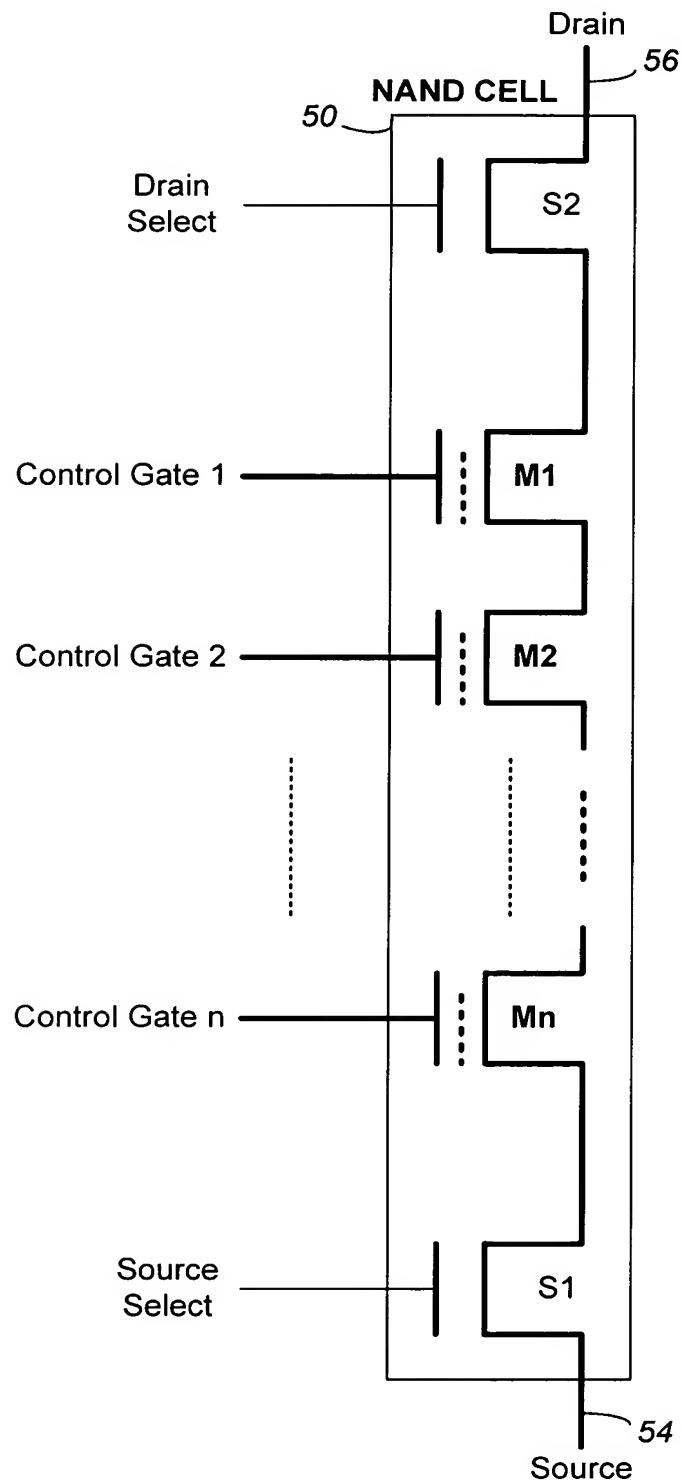
**FIG. 1B**



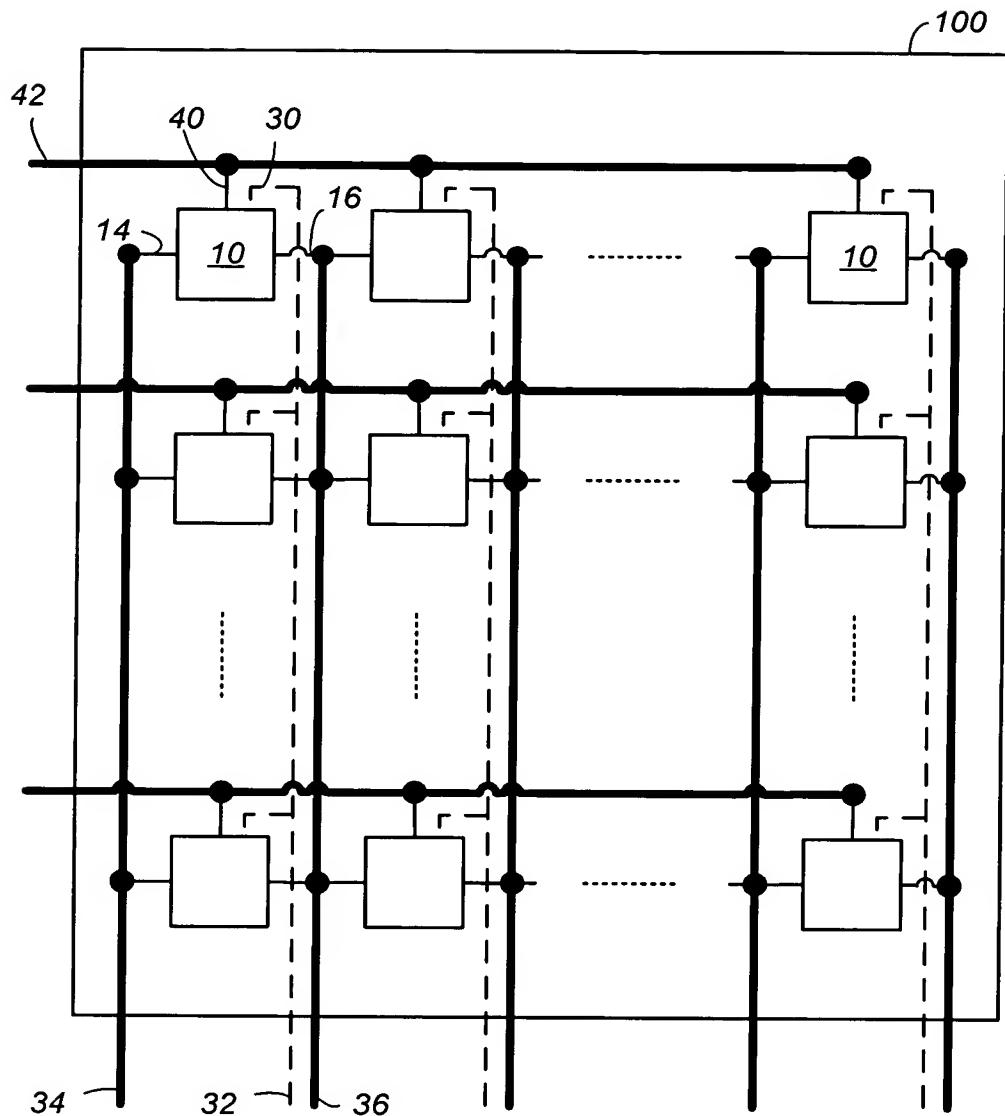
**FIG. 1C**



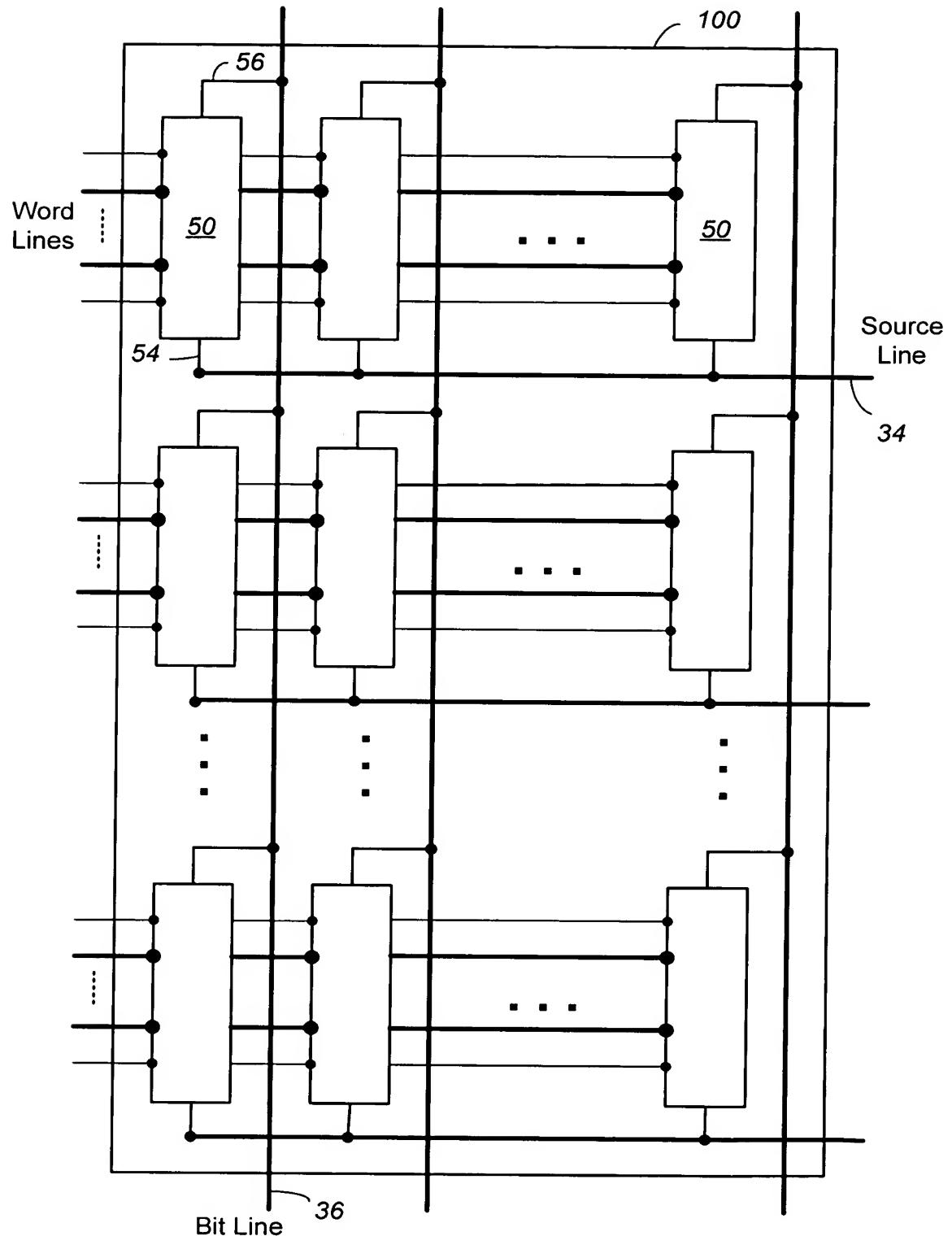
**FIG. 1E**



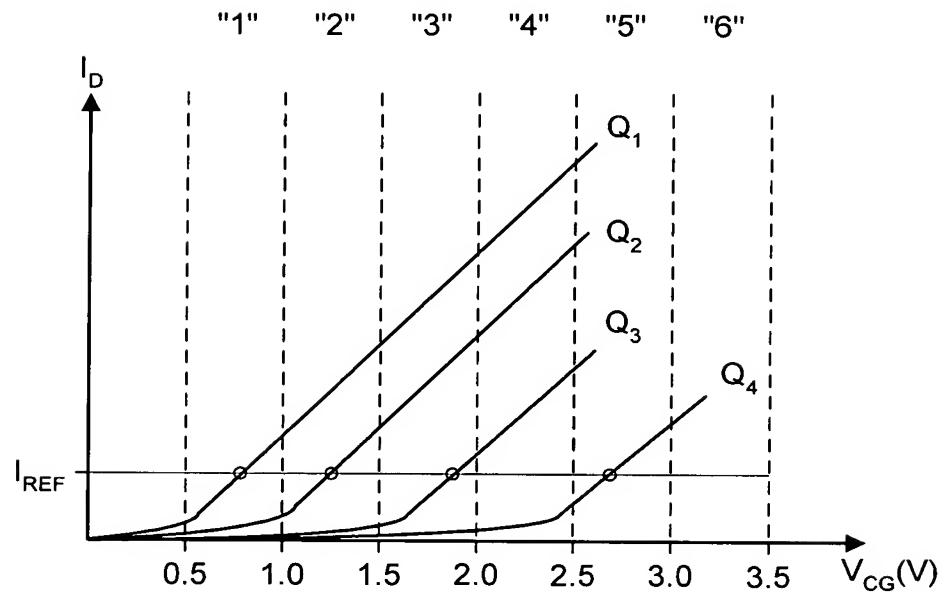
**FIG. 1D**



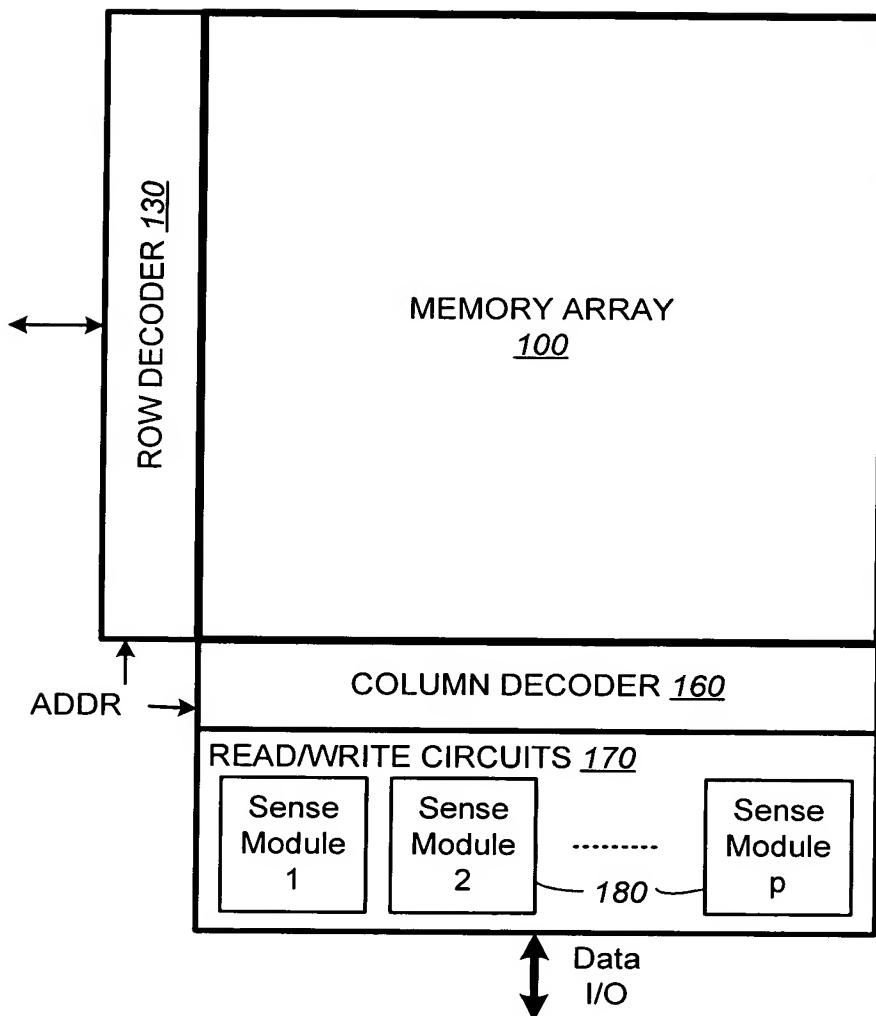
**FIG. 2**



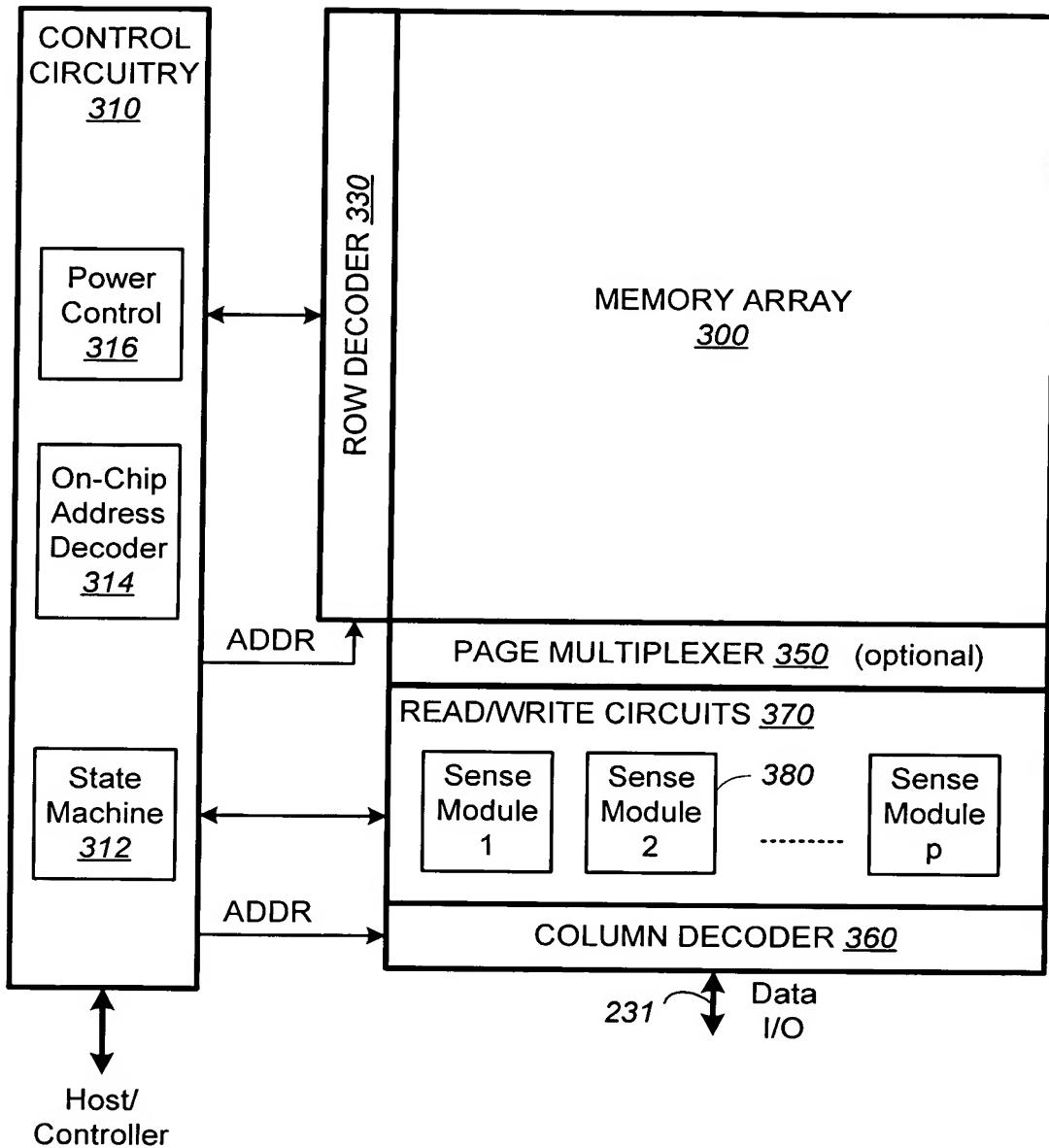
**FIG. 3**



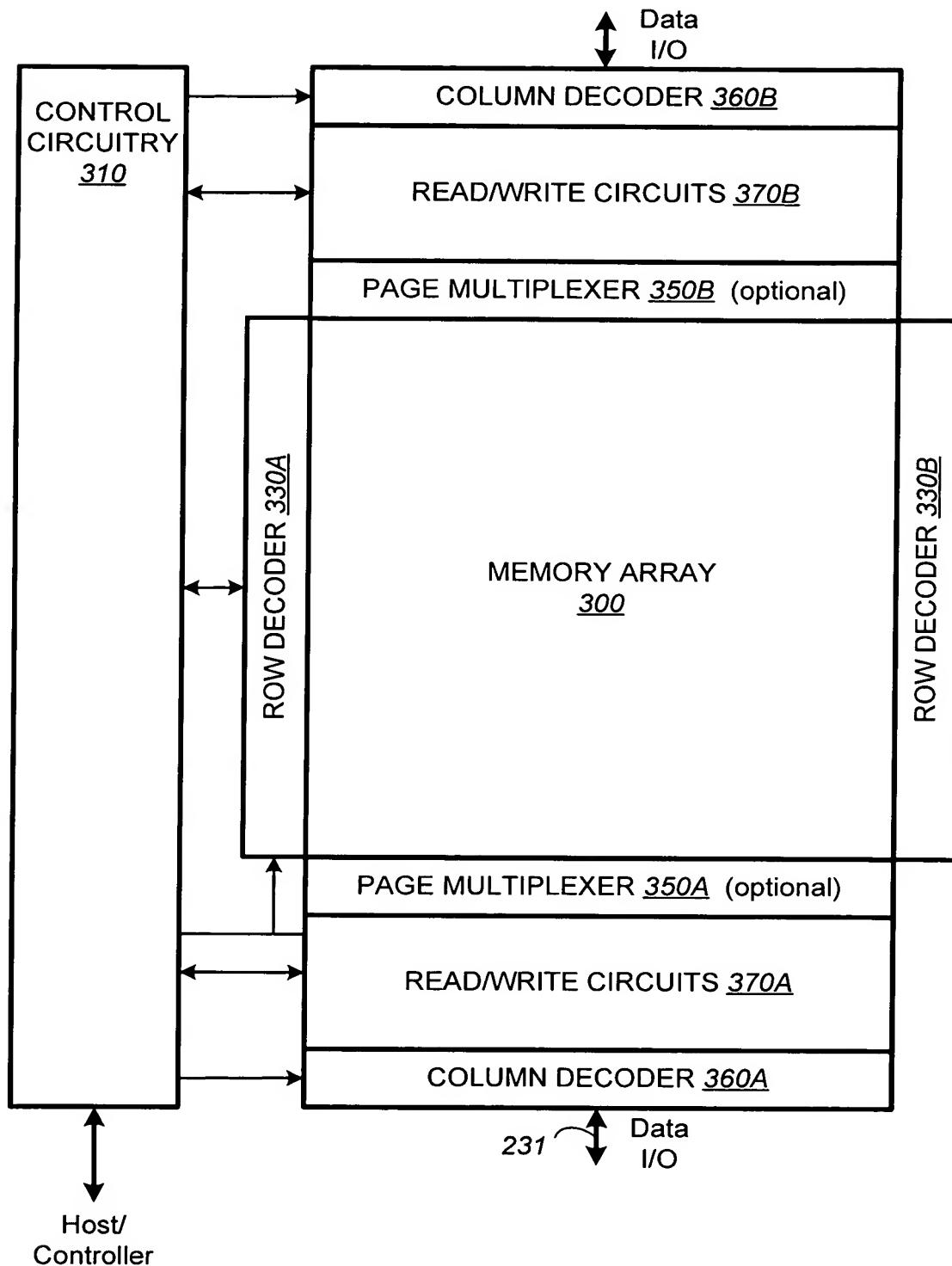
**FIG. 4**



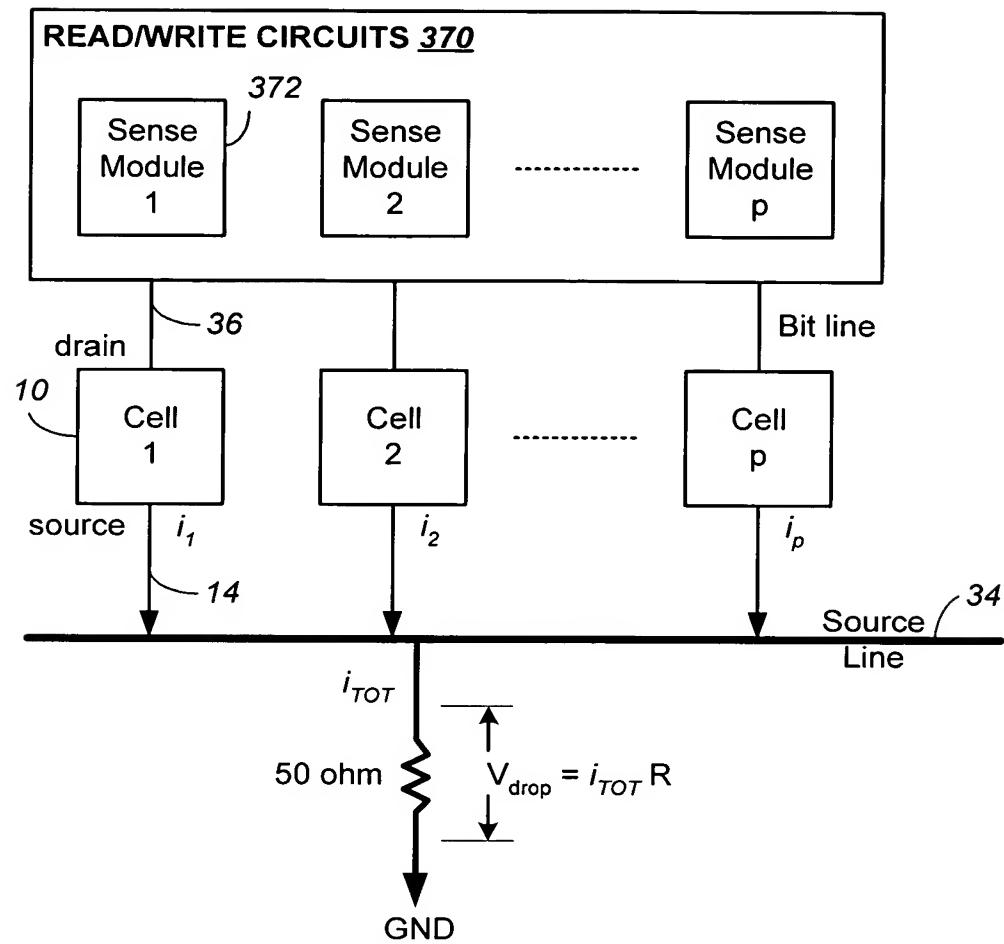
**FIG. 5**



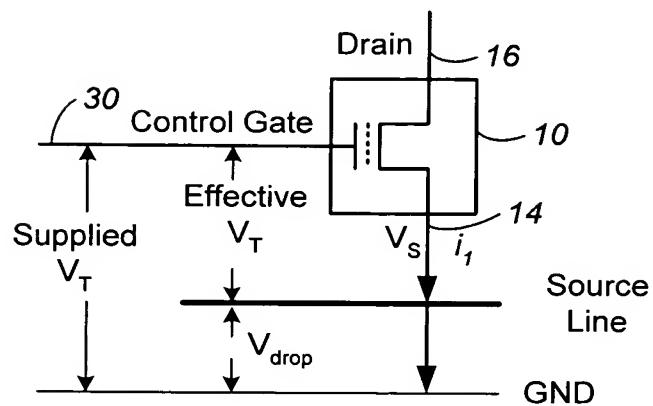
**FIG. 6A**



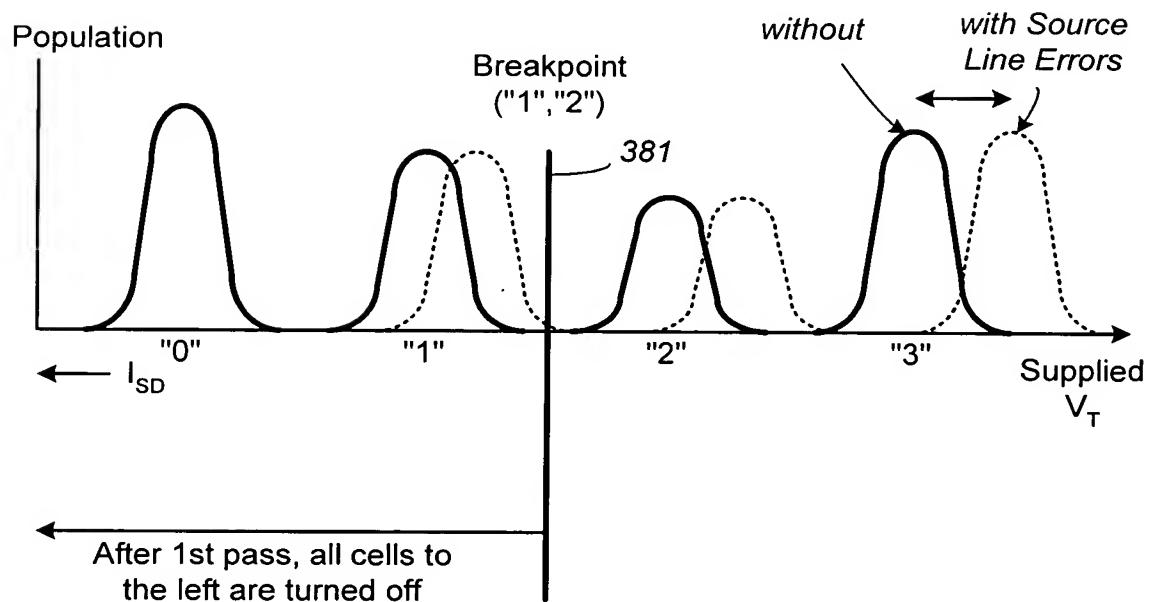
**FIG. 6B**



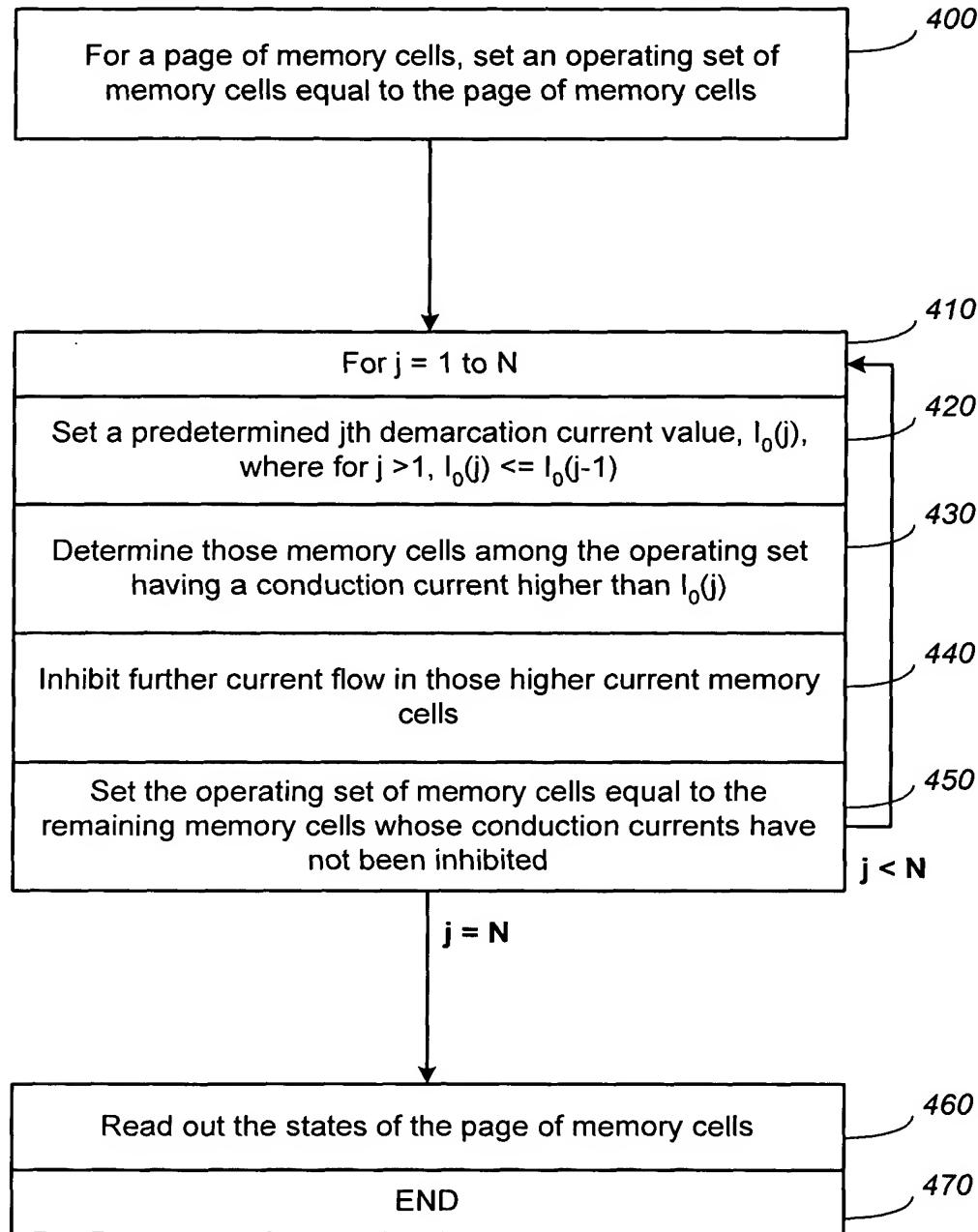
**FIG. 7A**



**FIG. 7B**

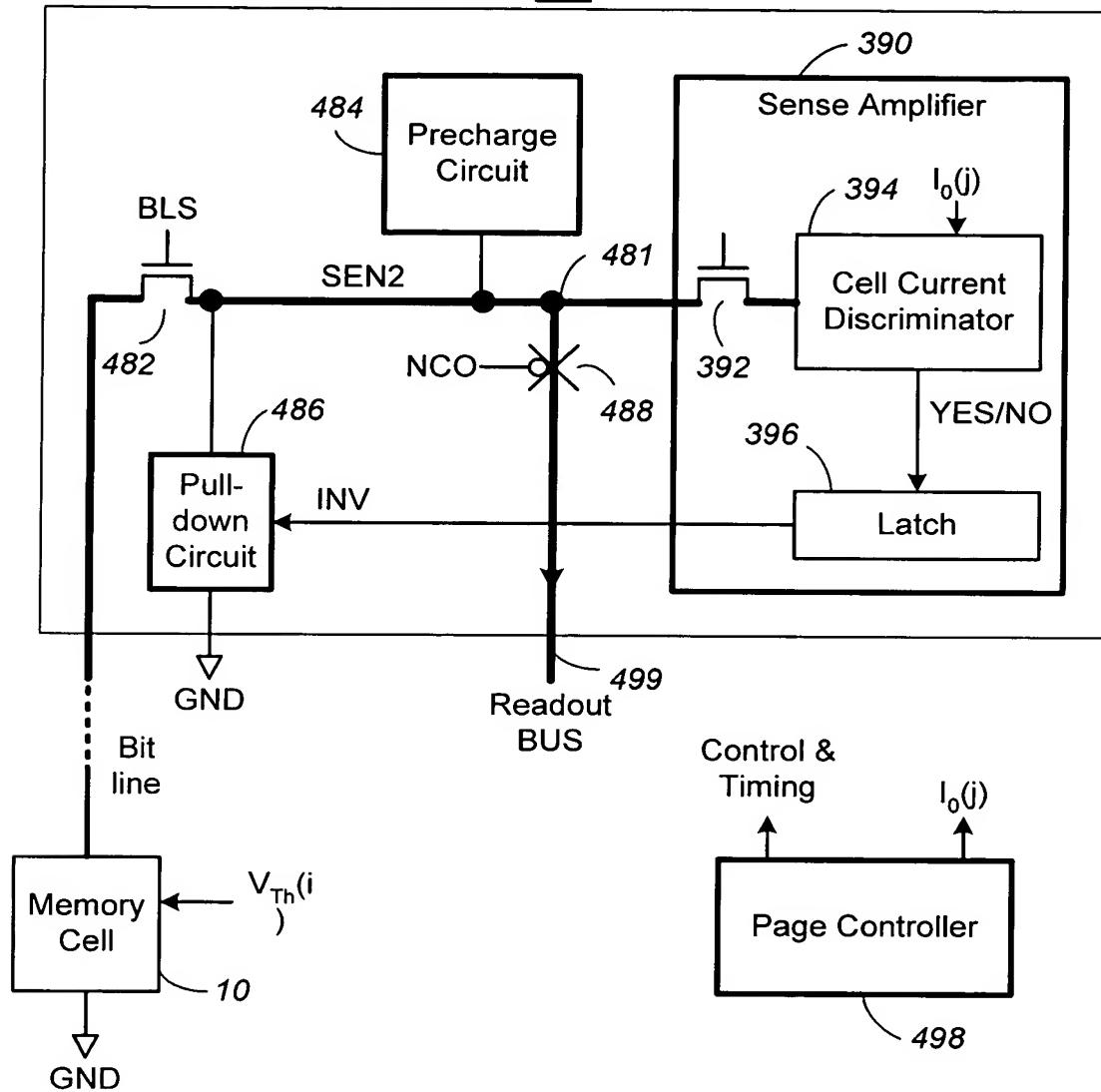


**FIG. 8**

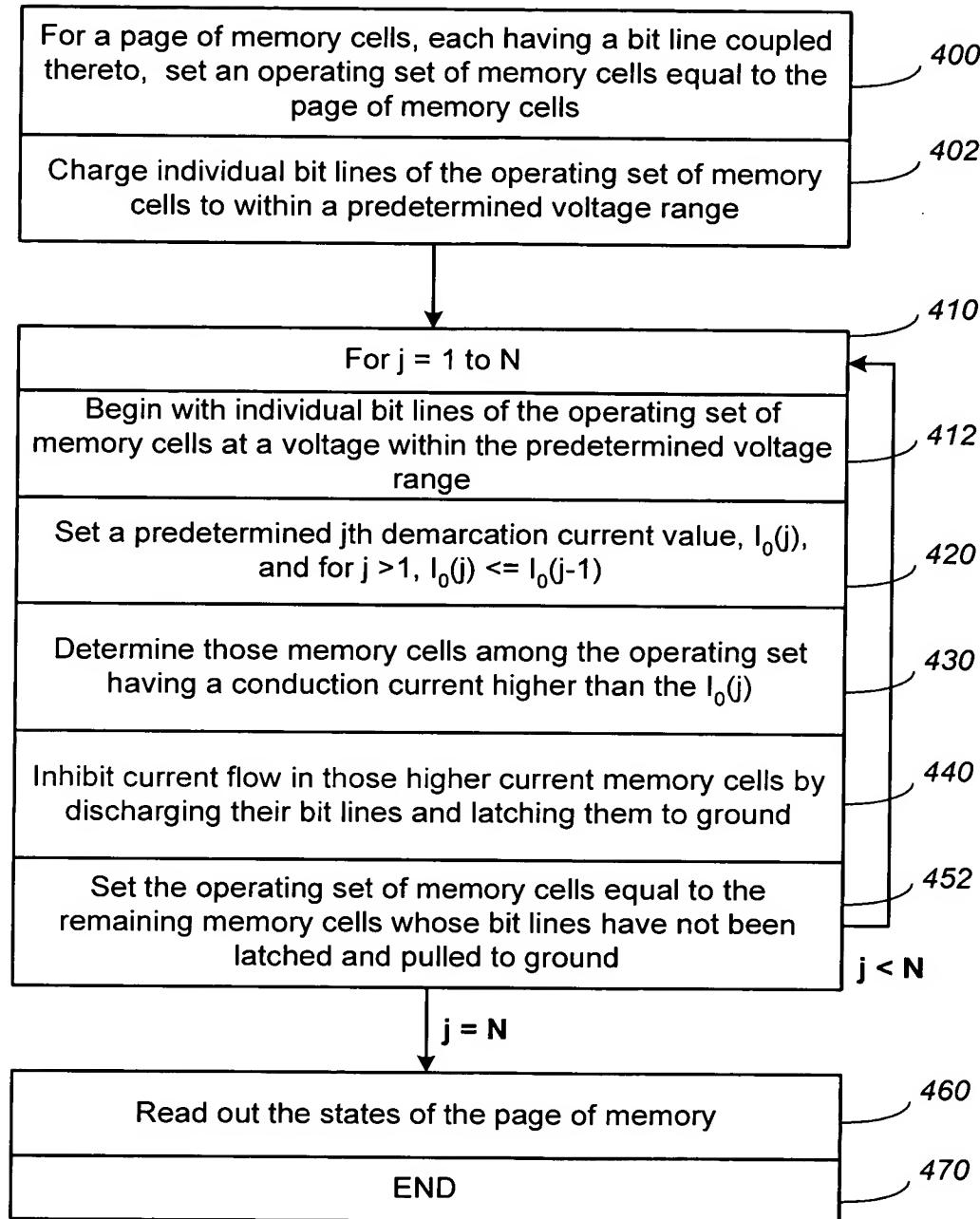


**FIG. 9**

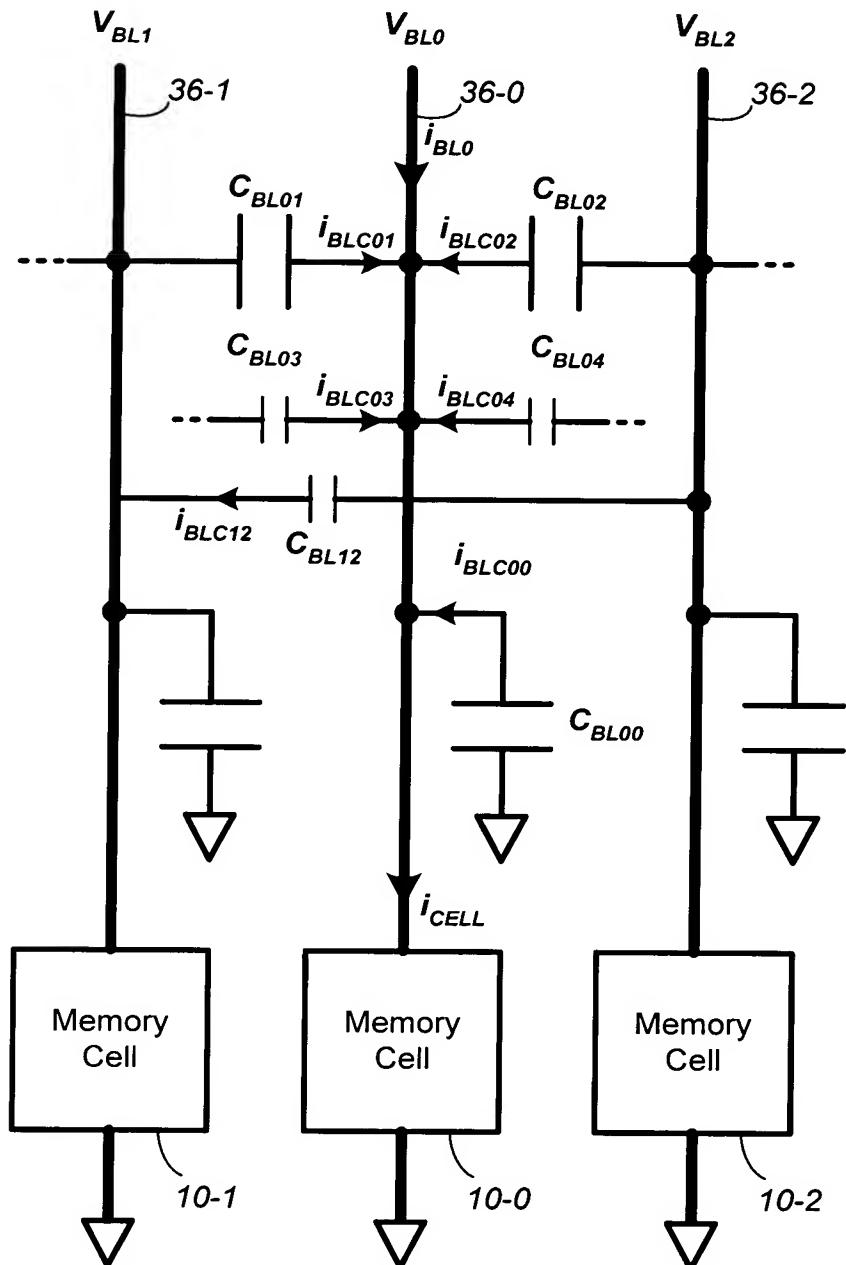
MULTI-PASS SENSE MODULE 380



**FIG. 10**

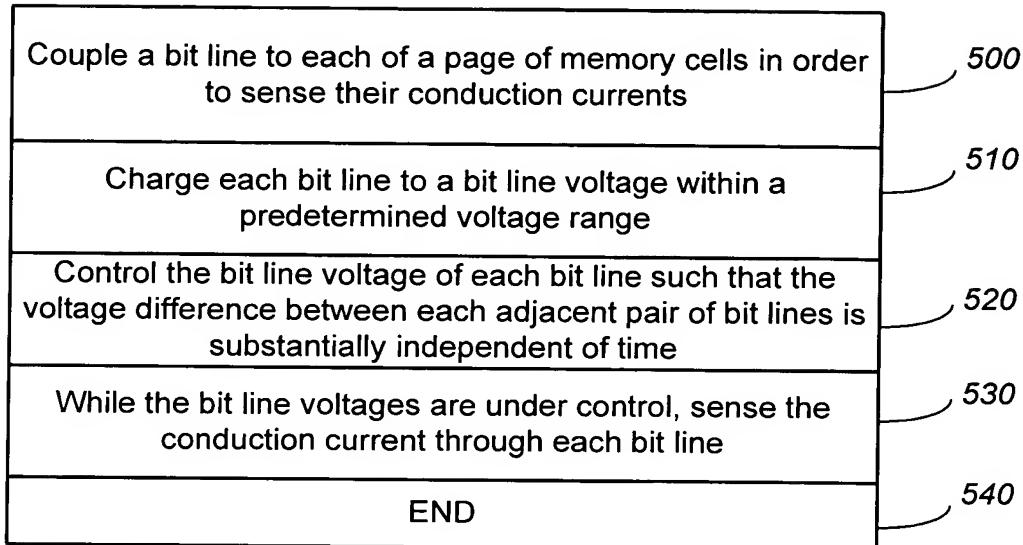


**FIG. 11**

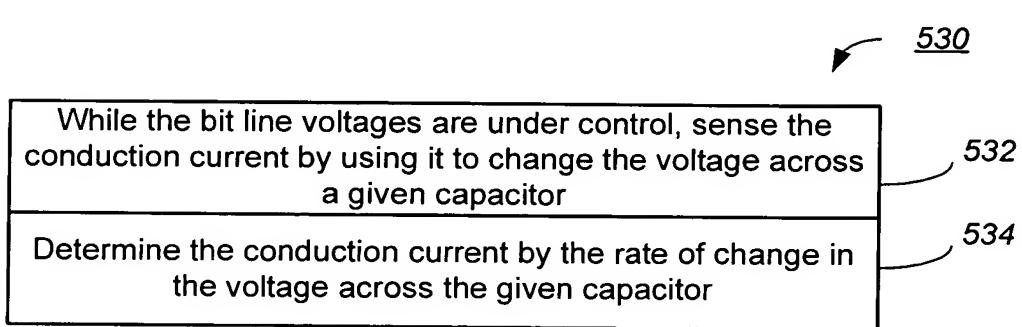


$$i_{CELL} = i_{BL0} + [i_{BLC00} + i_{BLC01} + i_{BLC02} + i_{BLC03} + i_{BLC04}]$$

**FIG. 12**



**FIG. 13A**



**FIG. 13B**

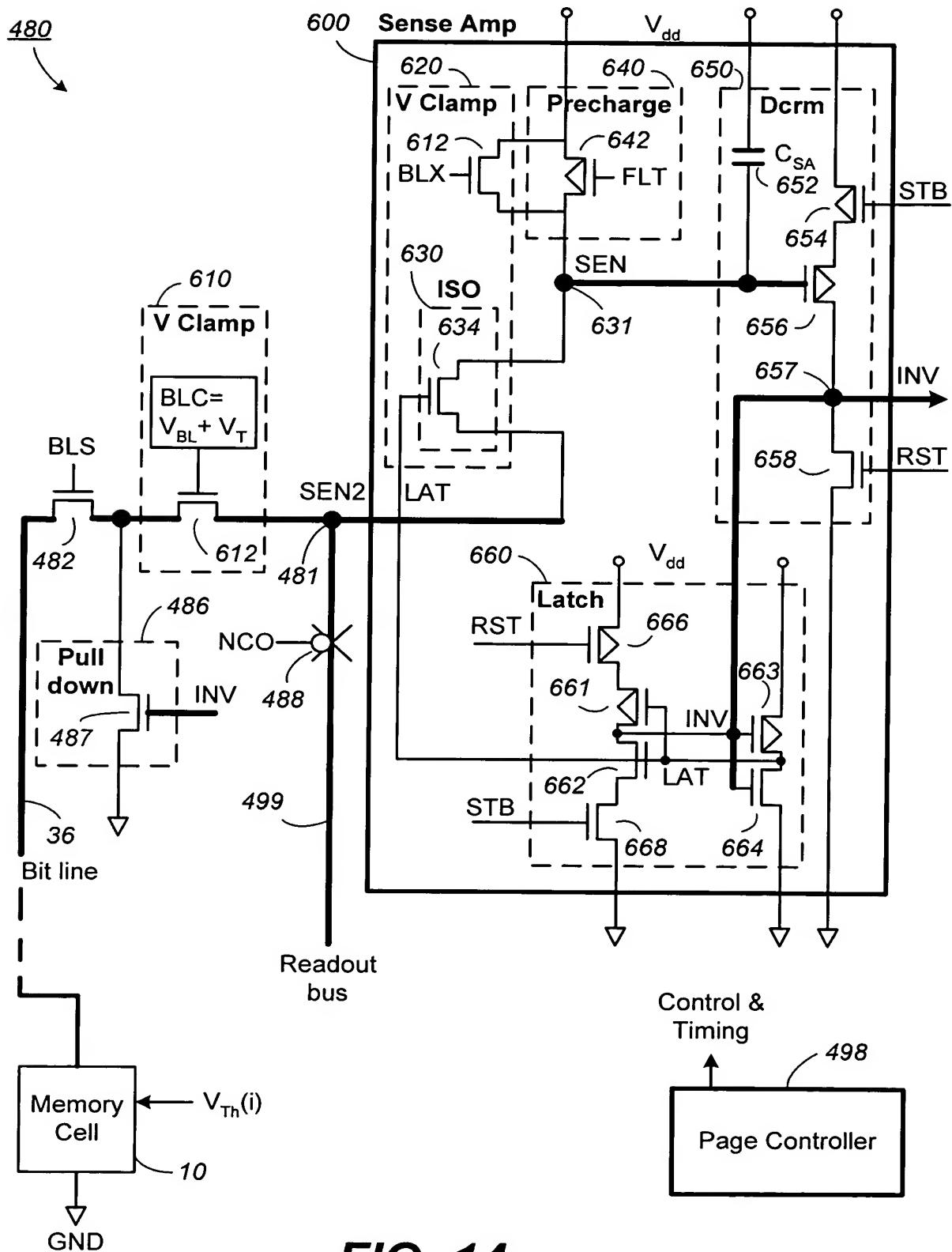
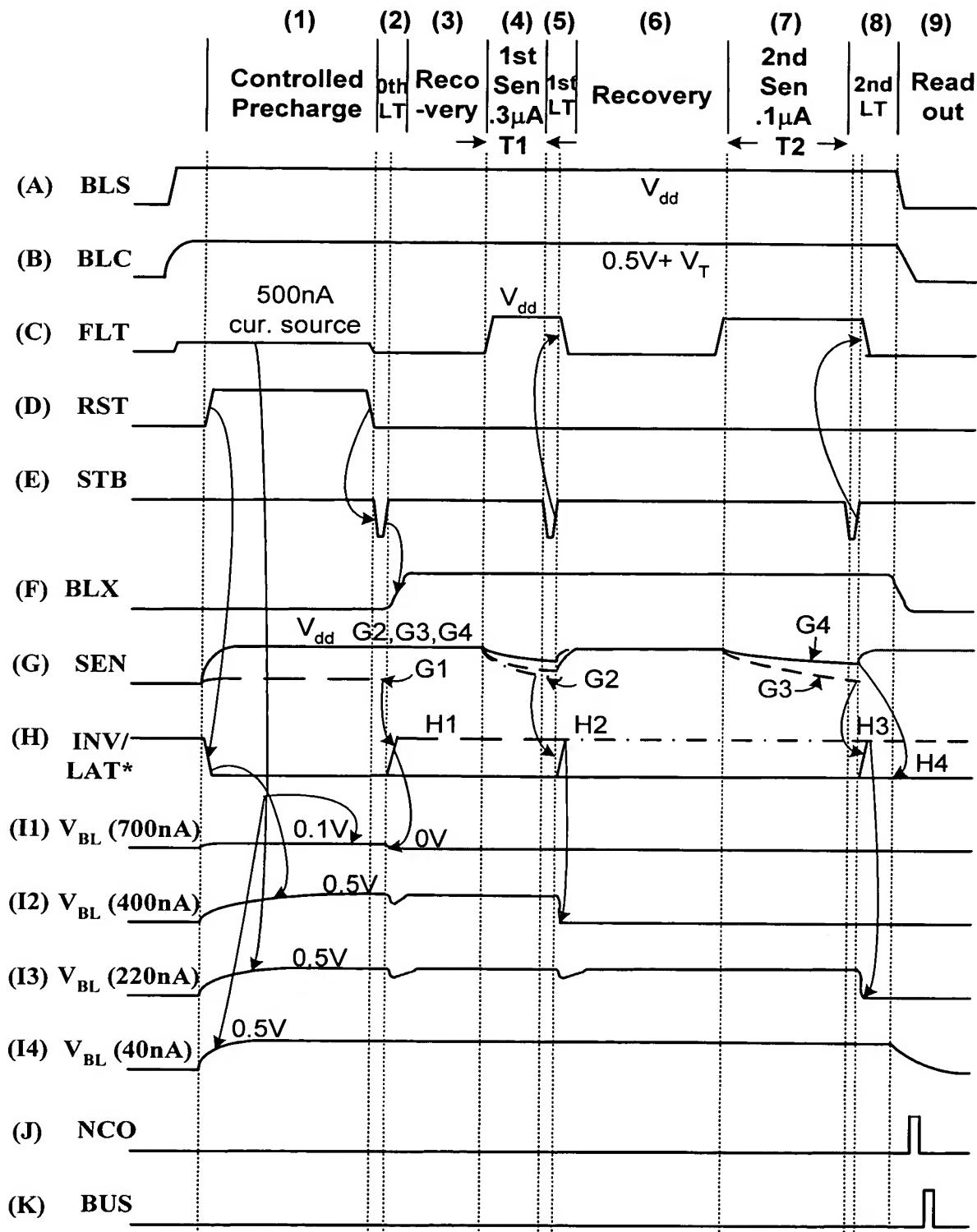
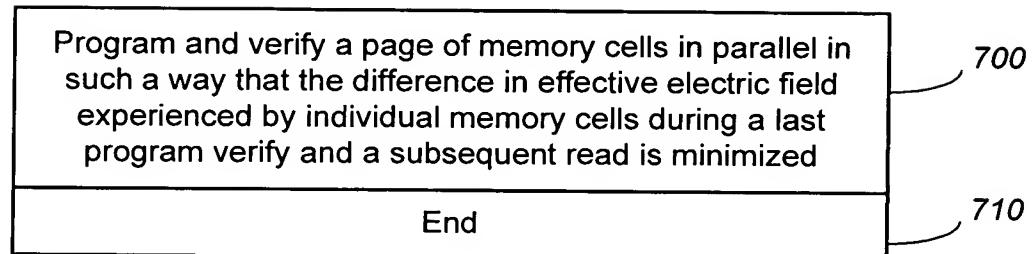


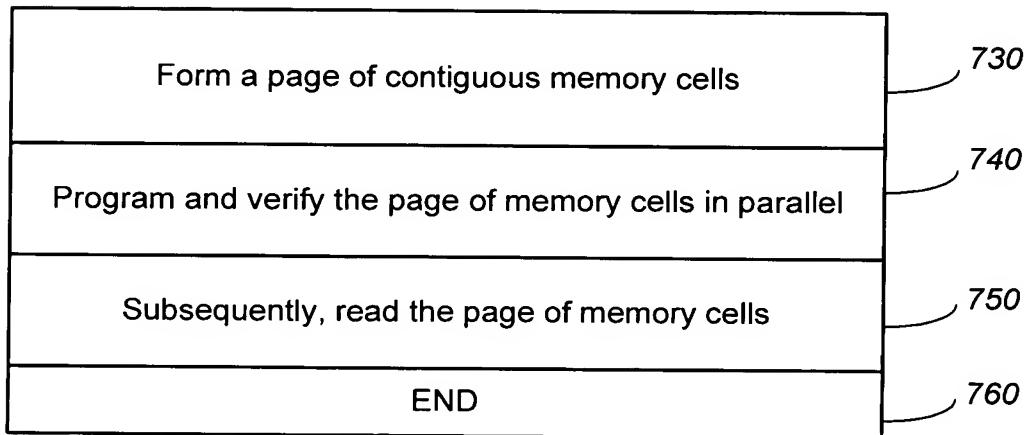
FIG. 14



**FIG. 15**

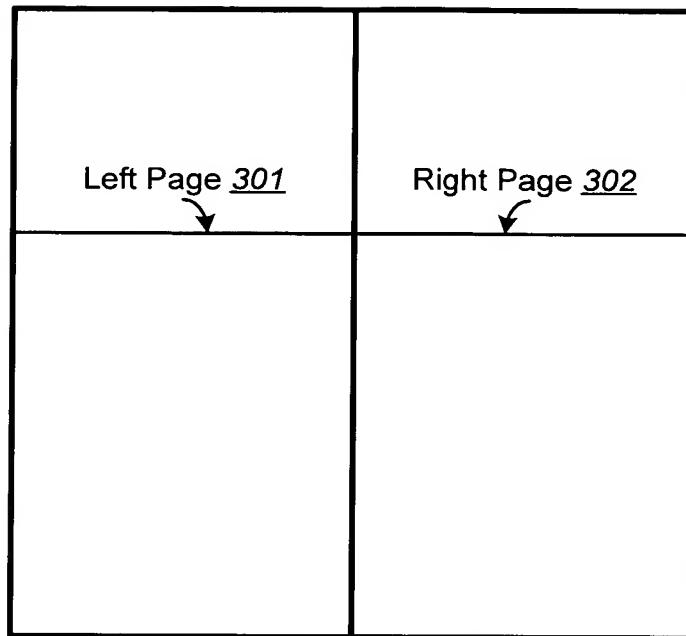


**FIG. 16A**

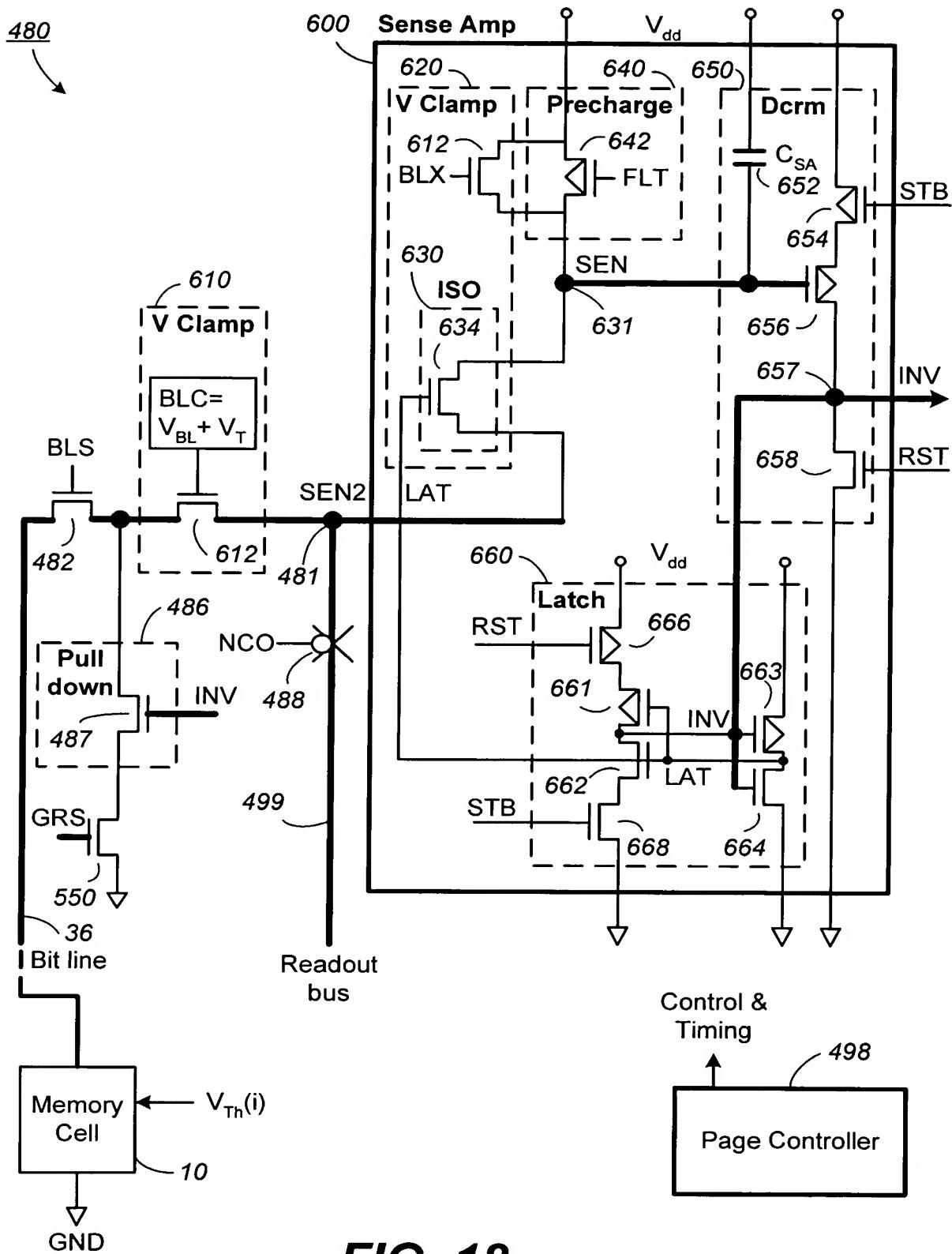


**FIG. 16B**

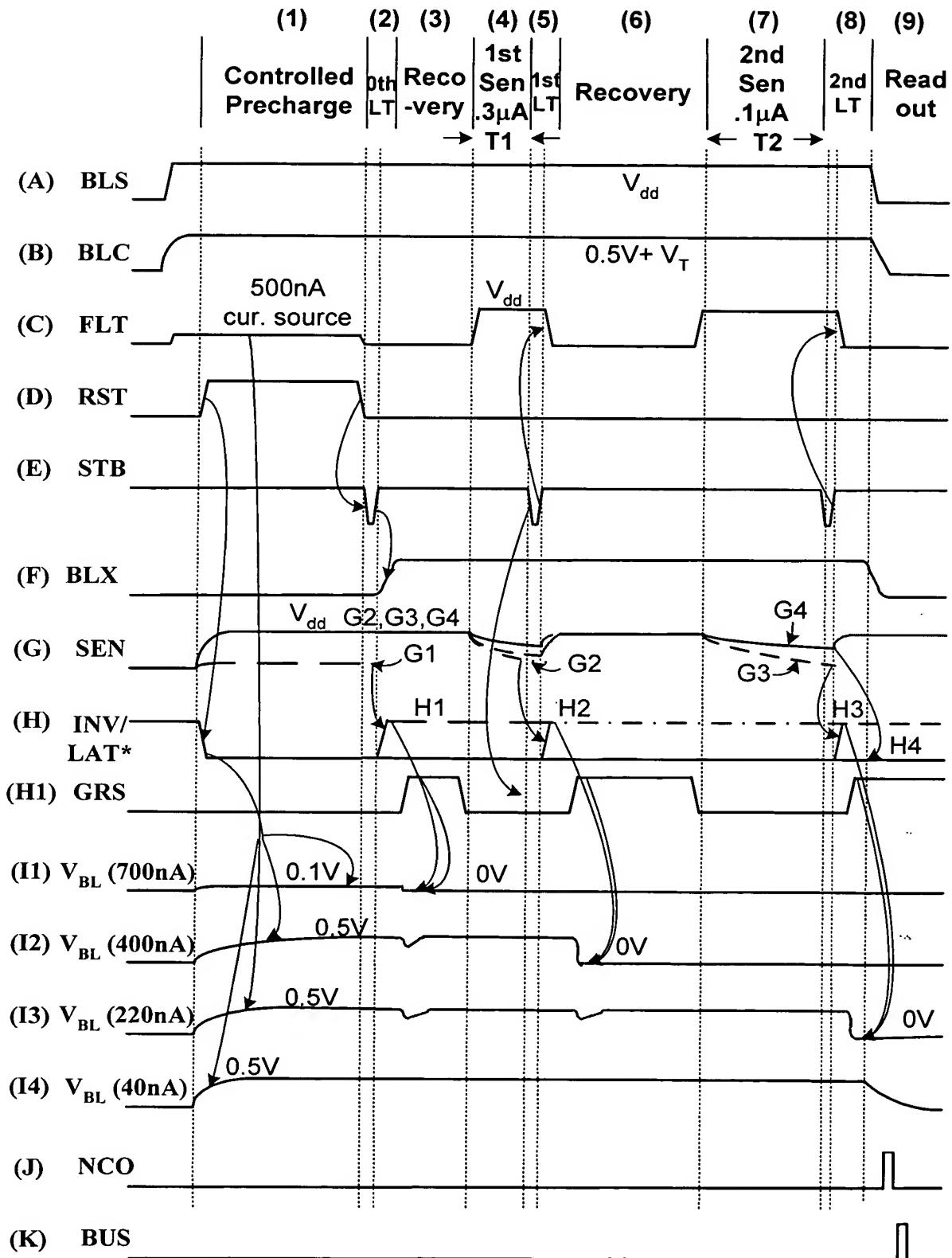
**MEMORY ARRAY 300**



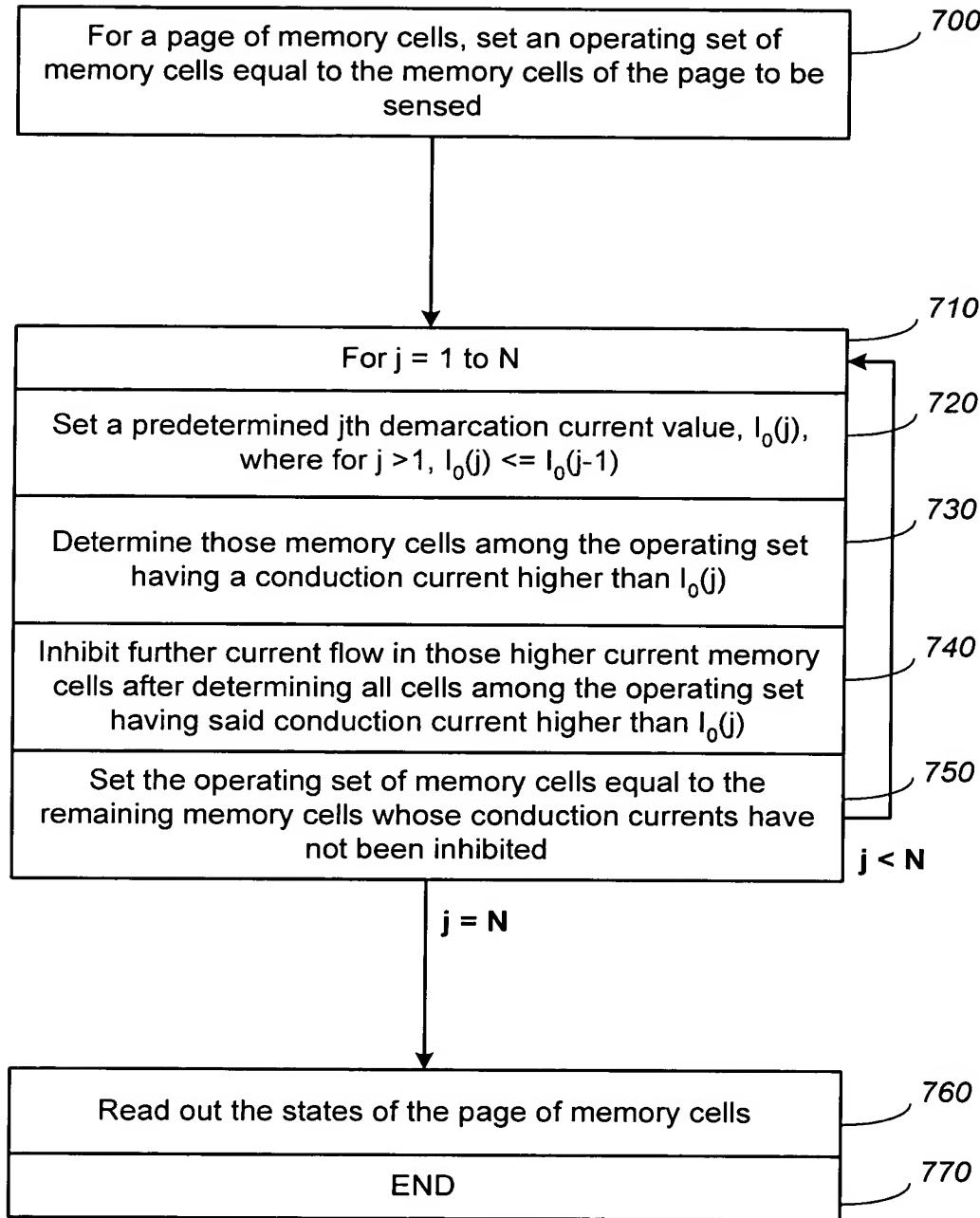
**FIG. 17**



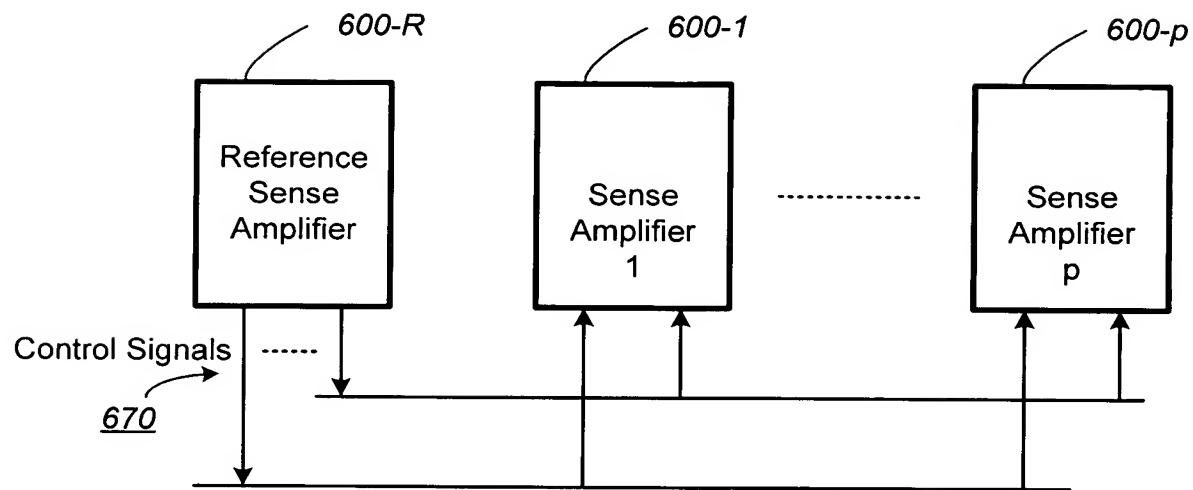
**FIG. 18**



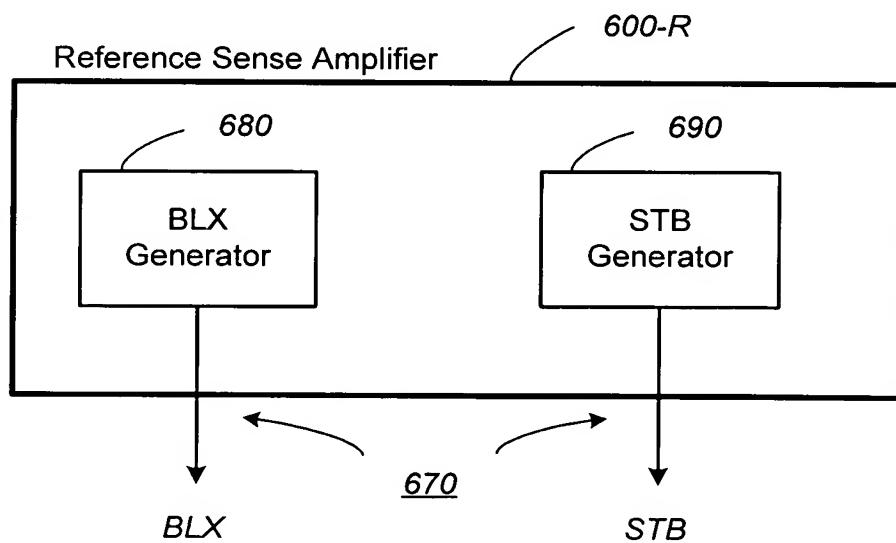
**FIG. 19**



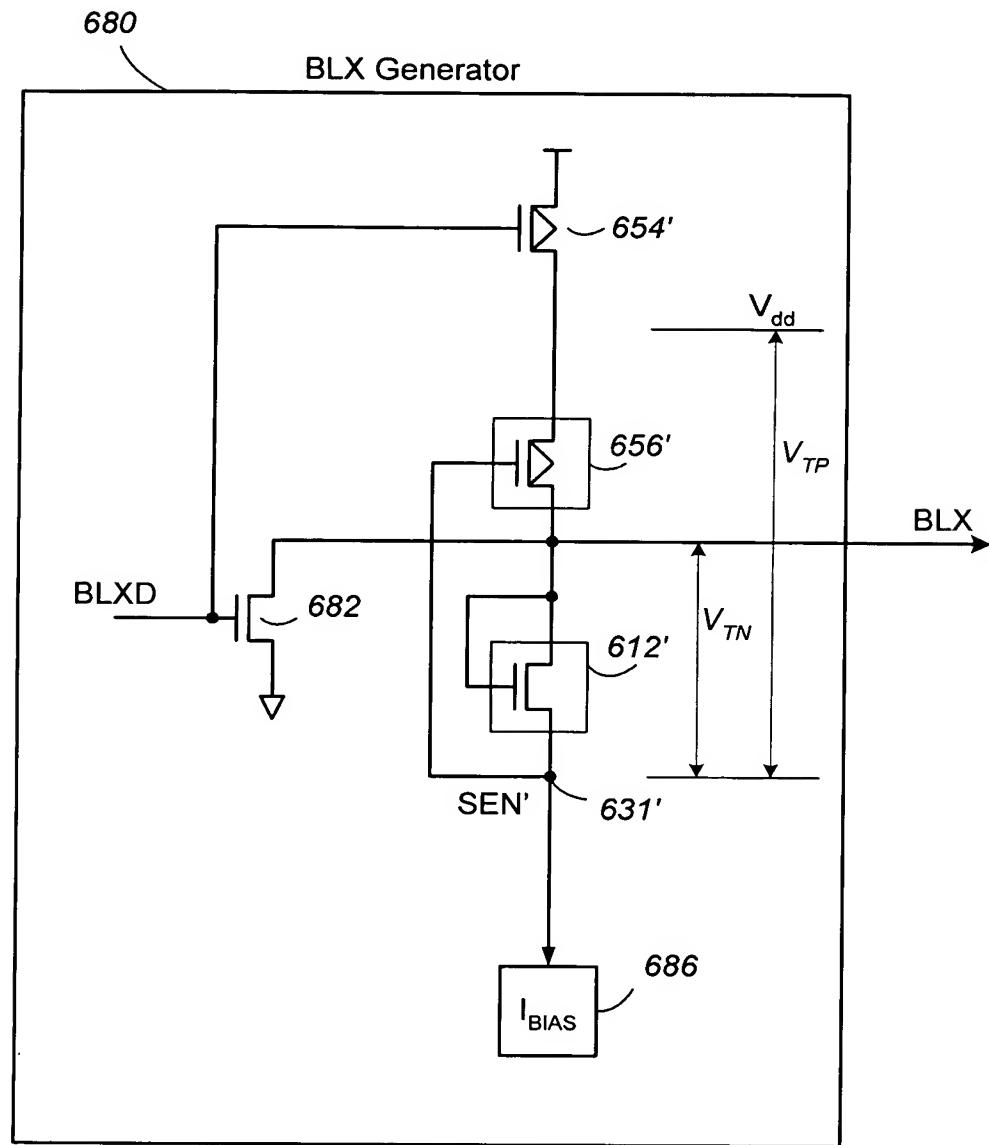
**FIG. 20**



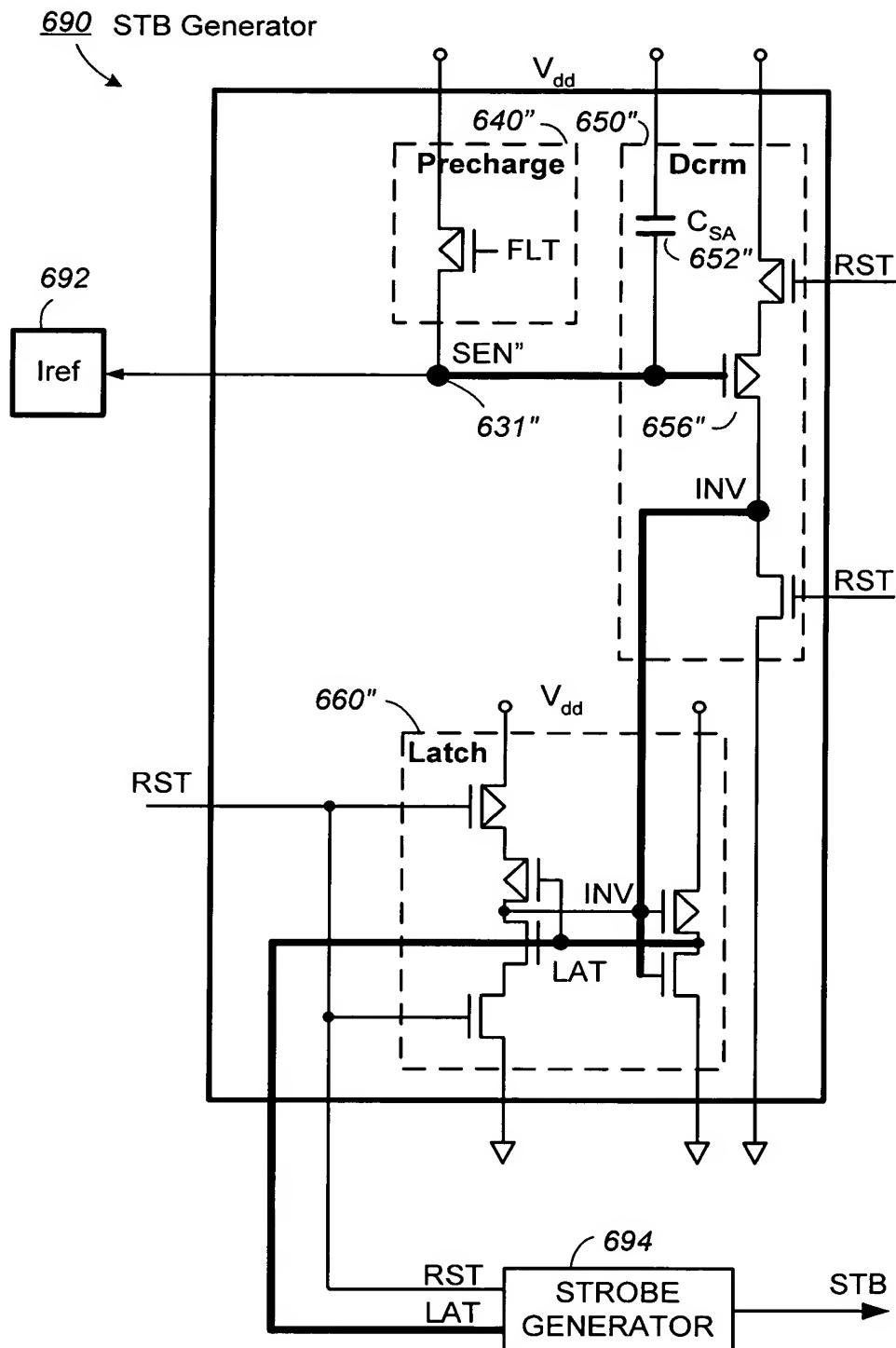
**FIG. 21A**



**FIG. 21B**



**FIG. 22**



**FIG. 23**